

Figure 1 (Prior art)

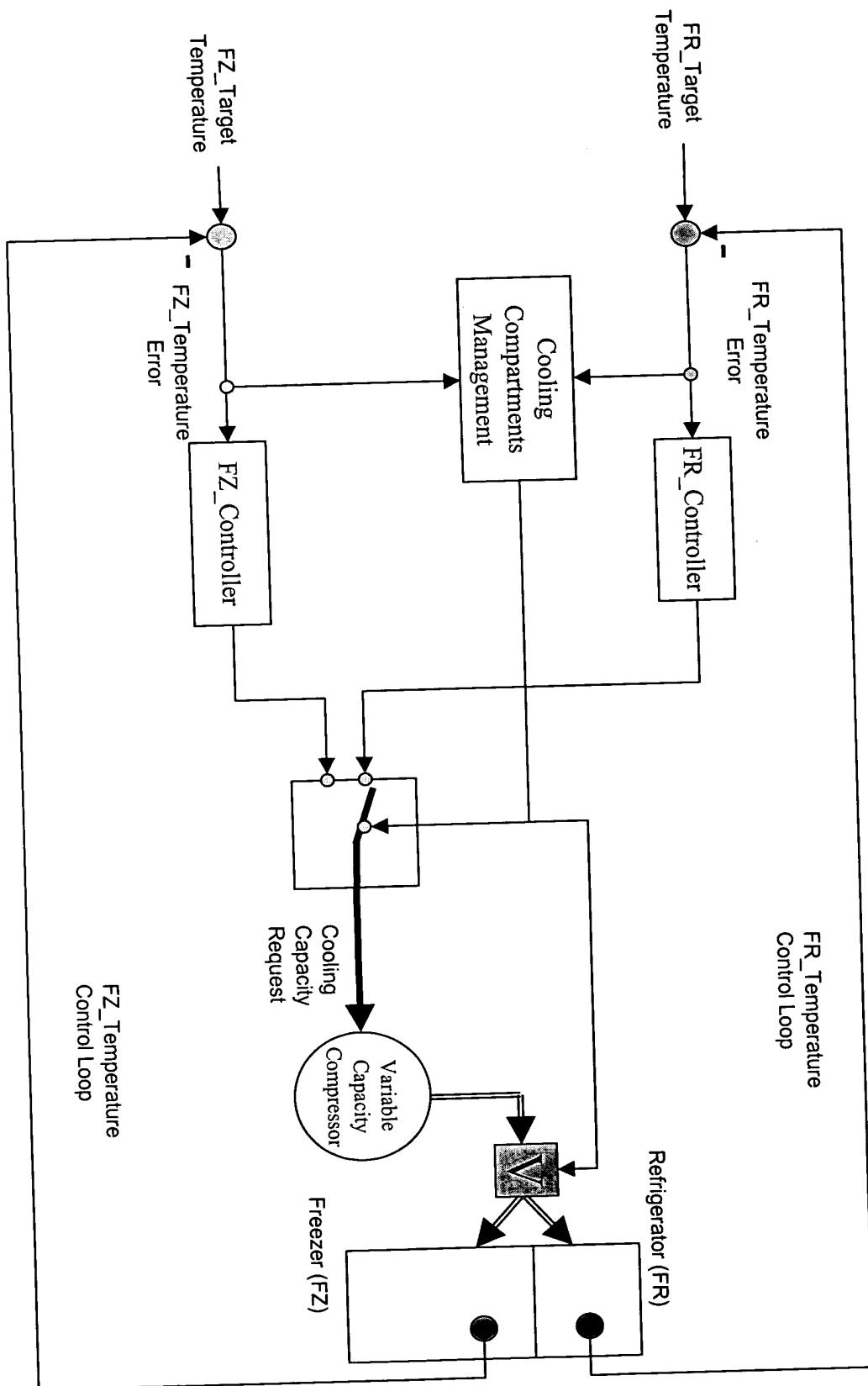


Figure 2

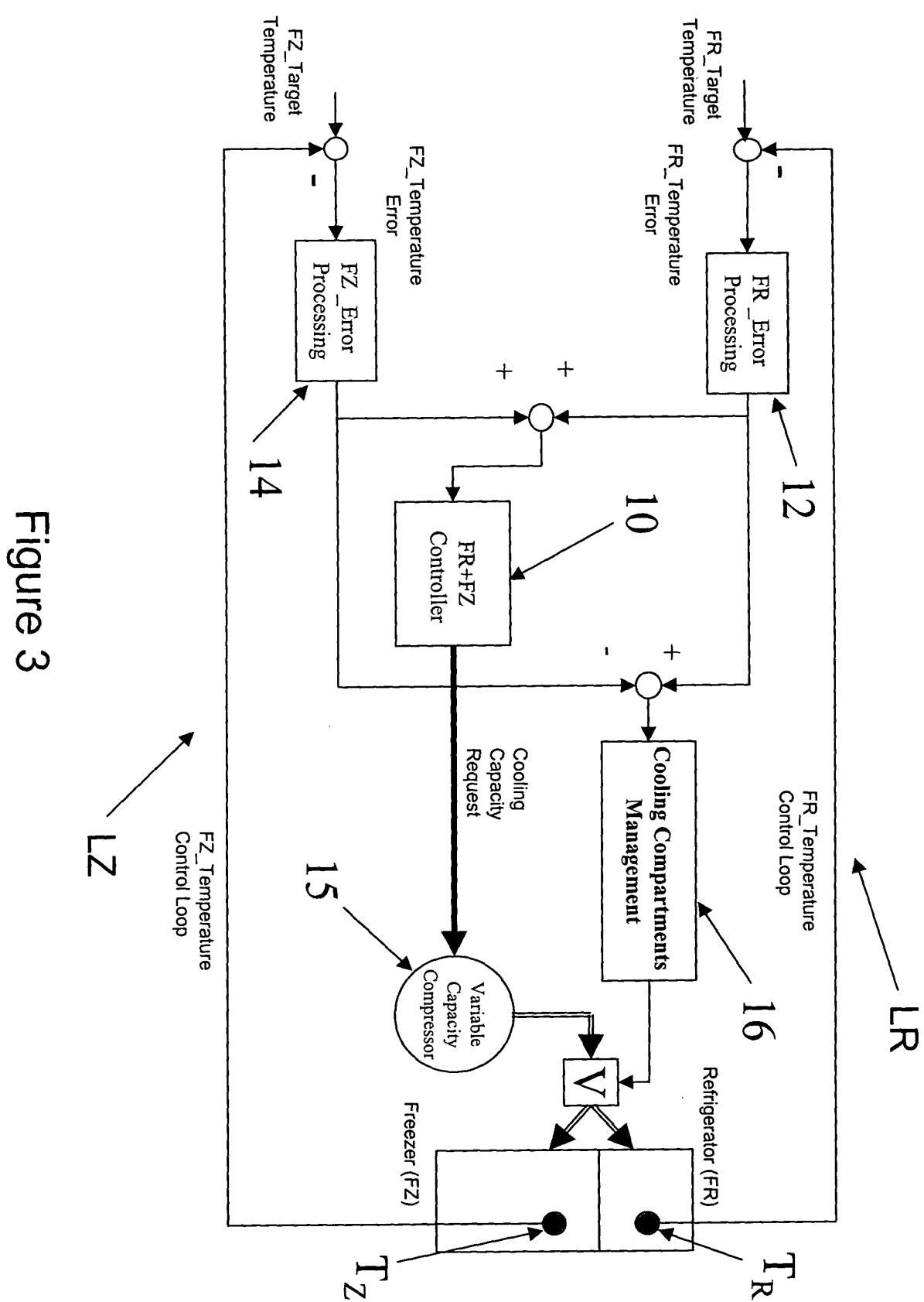


Figure 3

Independent controls

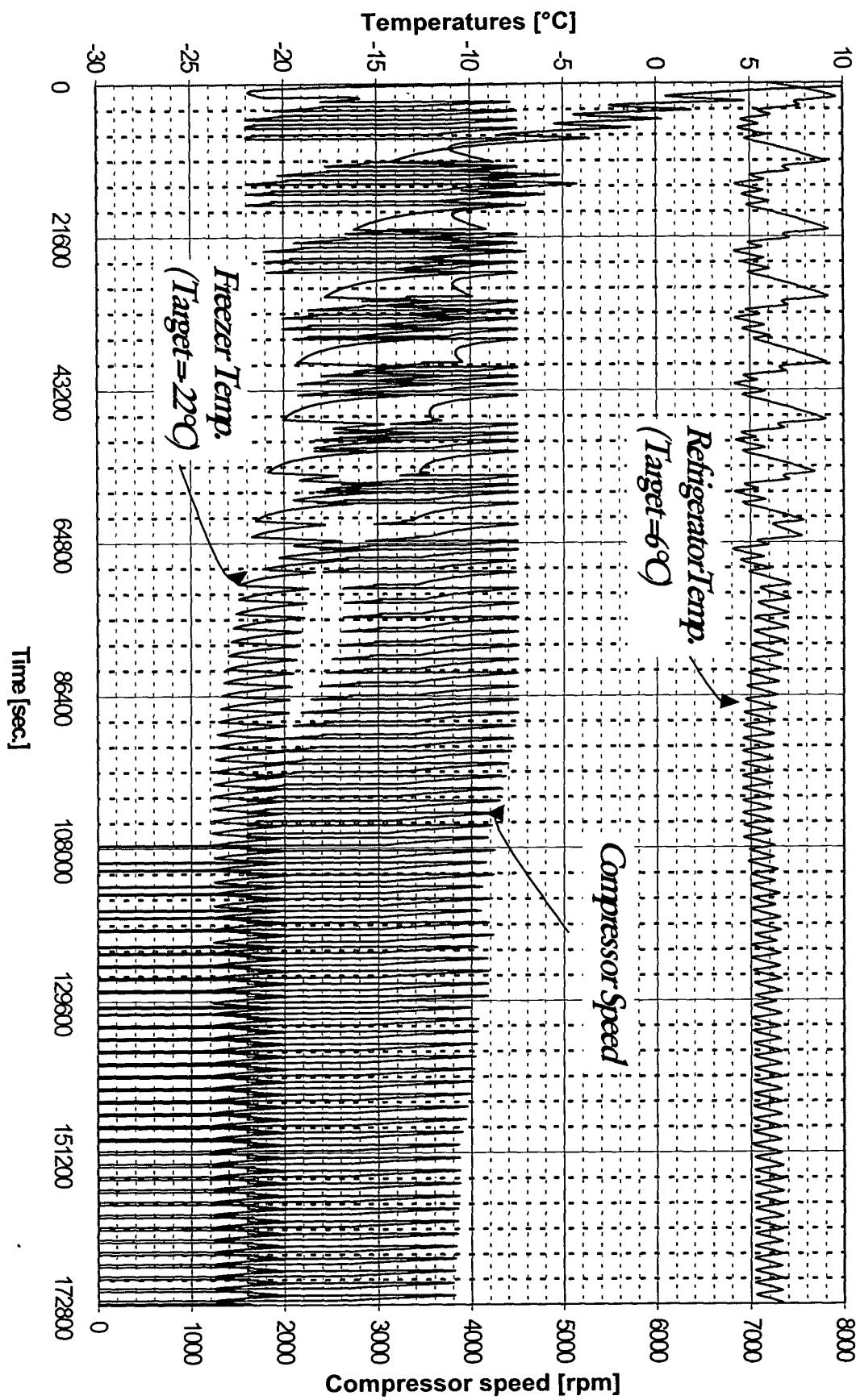


Figure 4

Error Sum

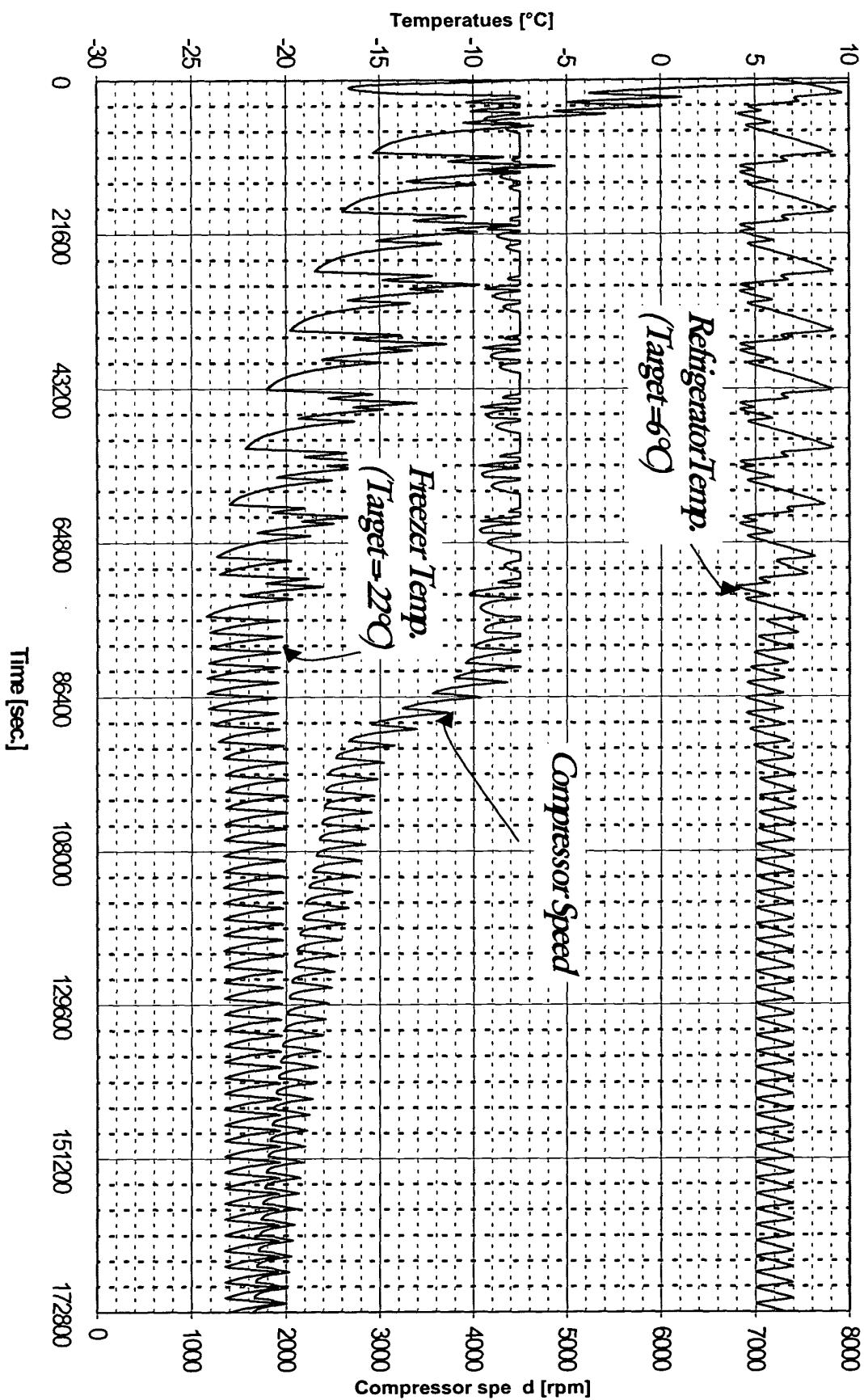


Figure 5

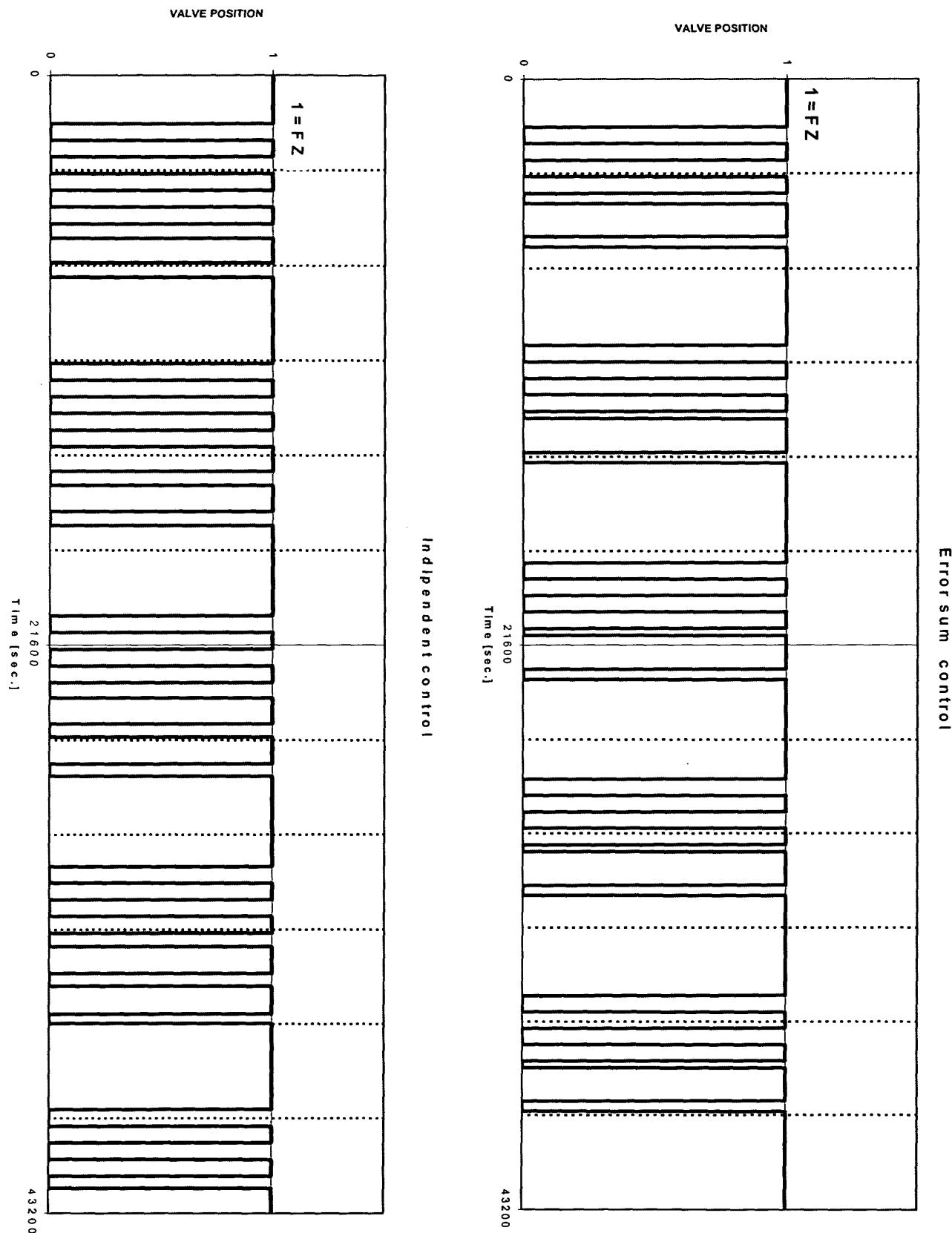


Figure 6

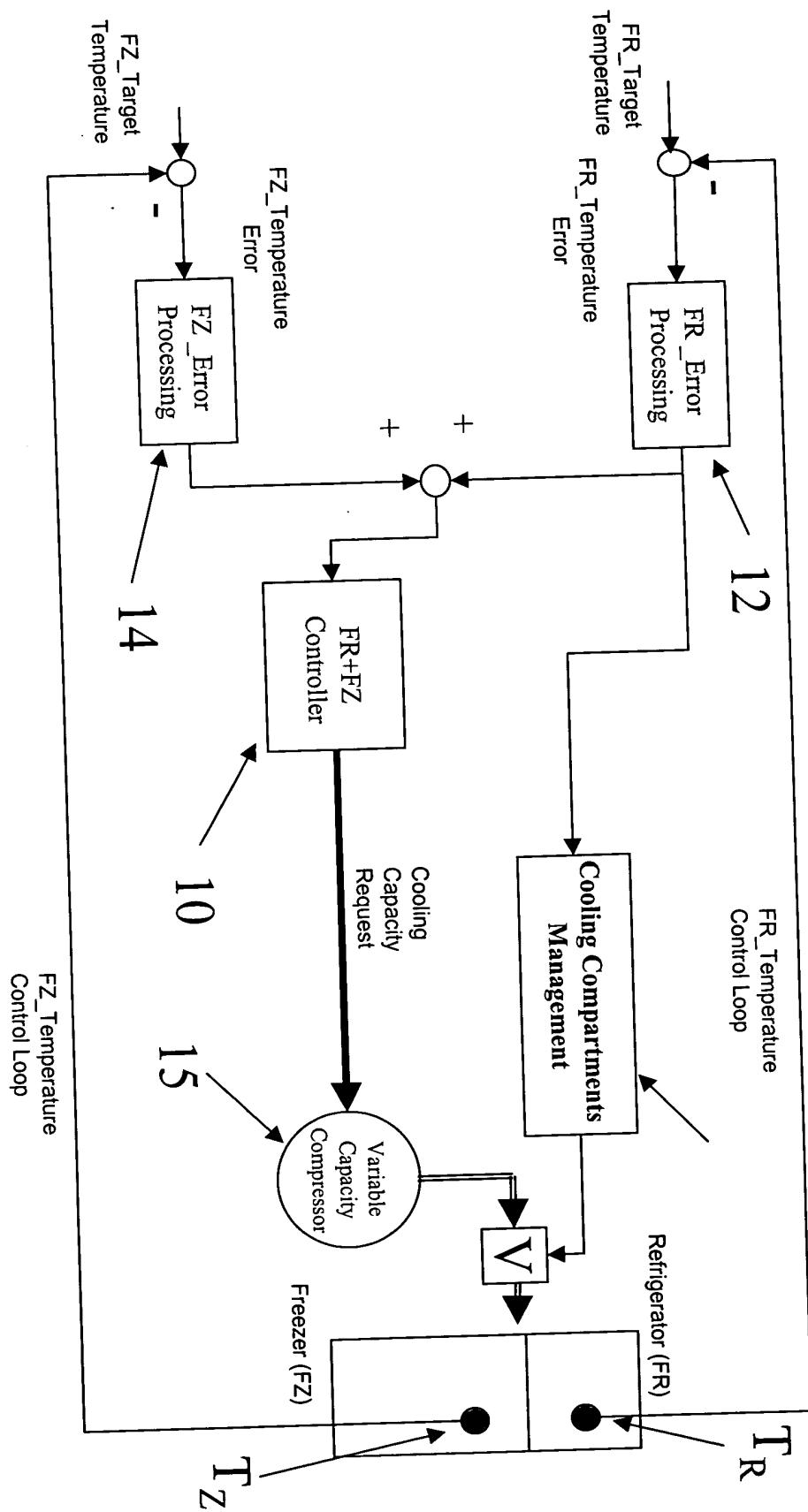


Figure 7

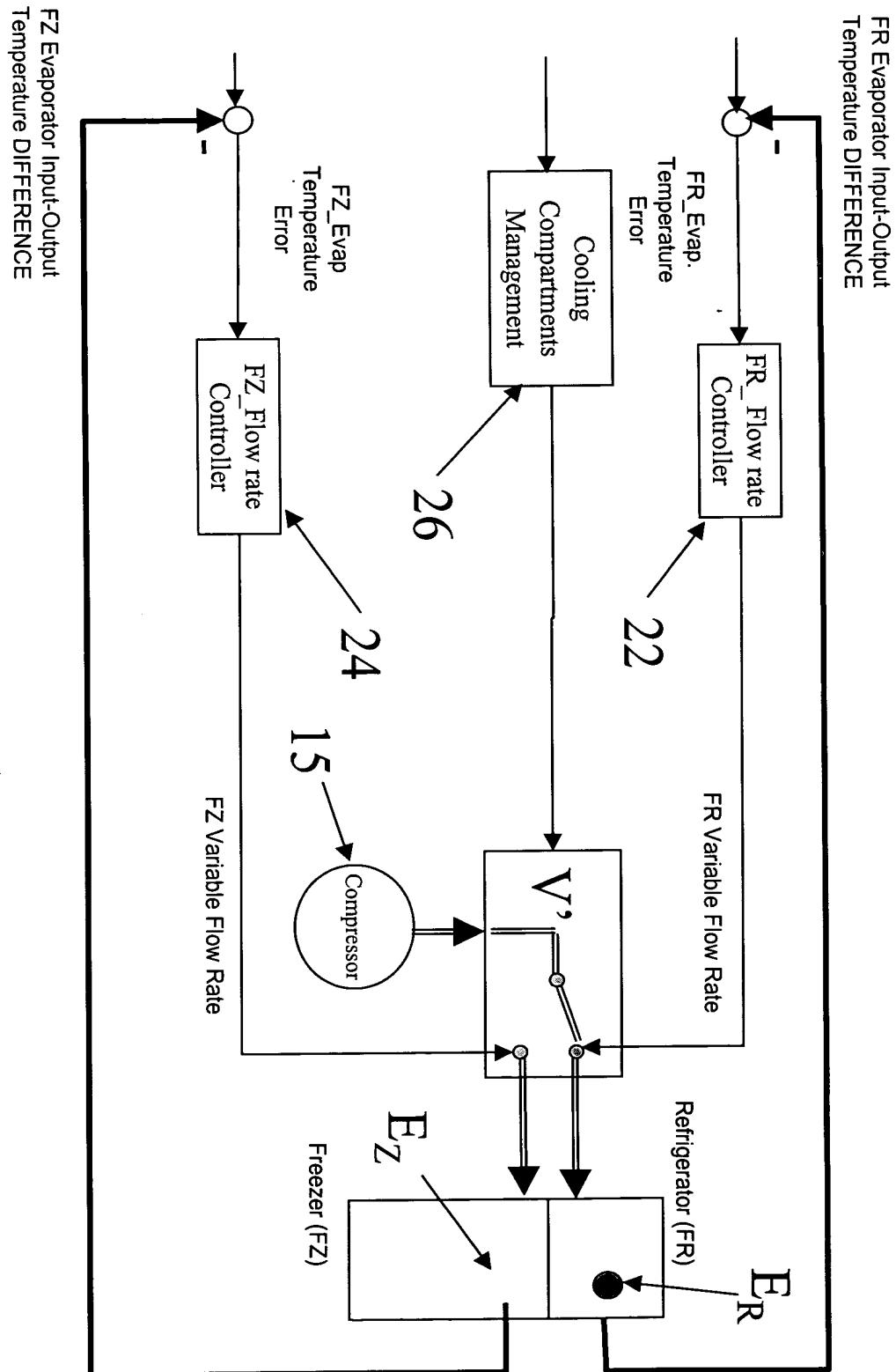


Figure 8